

**ASIAN SCHOOL OF TECHNOLOGY**  
**TIME TABLE (5<sup>TH</sup> SEMESTER WINTER - 2025)**

**BRANCH: E&TC ENGG.**

DAY	10.00A.M- 11.00A.M	11.00A.M- 12.00.M	12.00P.M - 1.00P.M	1.00P.M - 2.00P.M	2.00P.M - 3.00P.M	3.00P.M - 4.00P.M
MONDAY	WP & BCE	EM&ST	VLSI & ES	R	POWER ELECTRONICS & PLC. LAB	
TUESDAY	A&DC	PE & PLC	EM&ST	E	W P & B C ENGG.LAB	
WEDNESDAY	PE & PLC	VLSI & ES	A&DC	C	ANALOG & DIGITAL COMMUNICATION LAB	
THURSDAY	VLSI & ES	WP & BCE	EM&ST	E	POWER ELECTRONICS & PLC. LAB	
FRIDAY	A&DC	EM&ST	WP & BCE	S	VLSI & EMBEDDED SYSTEM LAB	
SATURDAY	PE & PLC	VLSI & ES	A&DC	S	-----	

<u>THEORY SUBJECTS</u>		
<u>SL.#</u>	<u>SUB.CODE</u>	<u>SUBJECT</u>
1.	TH - 1	ENTREPRENEURSHIP AND MANAGEMENT & SMART TECHNOLOGY
2.	TH - 2	VLSI & EMBEDDED SYSTEM
3.	TH - 3	ANALOG & DIGITAL COMMUNICATION
4.	TH - 4	WAVE PROPAGATION & BROADBAND COMM. ENGG.
5.	TH - 5	POWER ELECTRONICS & PLC

<u>PRACTICAL SUBJECTS</u>		
<u>SL.#</u>	<u>SUB.CODE</u>	<u>SUBJECT</u>
1.	PR-1	ANALOG & DIGITAL COMMUNICATION LAB
2.	PR -2	VLSI & EMBEDDED SYSTEM LAB
3.	PR -3	W P & B C ENGG.LAB
4.	PR-4	POWER ELECTRONICS & PLC

Principal

**PRINCIPAL**

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